



Image

Op 2877

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JAMES BROC STIRTON  
KEVIN R. LENSING  
HORMUZDIAR E. NARIMAN  
STEVEN P. REEVES

Serial No.: 10/084,987

Filed: February 28, 2002

For: METHOD OF USING HIGH YIELDING  
SPECTRA SCATTEROMETRY  
MEASUREMENTS TO CONTROL  
SEMICONDUCTOR MANUFACTURING  
PROCESSES, AND SYSTEM FOR  
ACCOMPLISHING SAME

Group Art Unit: 2877

Examiner: Roy M. Punnoose

Atty. Dkt. No.: 2000.092400/TT5002

Customer No.: 23720

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

CERTIFICATE OF MAILING  
37 C.F.R 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date below:

November 24, 2003  
\_\_\_\_\_  
Date

  
\_\_\_\_\_  
Signature

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be

construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.97(e)(1), Applicants hereby certify that each item of information contained in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the present statement, as evidenced by the date of the enclosed search report.

Applicants respectfully request that the listed documents be made of record in the present case.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON  
CUSTOMER NO. 23720

Date: November 24, 2003

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Form PTO-1449 (modified)



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Atty. Docket No.

2000.092400/TT5002

Serial No.

10/084,987

**List of Patents and Publications for Applicant**  
**SUPPLEMENTAL**  
**INFORMATION DISCLOSURE STATEMENT**

(Use several sheets if necessary)

**Applicant**

James Broc Stirton, Kevin R. Lensing, Hormuzdiar E. Nariman and Steven P. Reeves

Filing Date:  
February 28, 2002Group:  
2877**U.S. Patent Documents**

See Page 1

**Foreign Patent Documents**

See Page 1

**Other Art**

See Page 1

**U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1						
	A2						
	A3						
	A4						
	A5						

**Foreign Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1	EP 0 727 715 A1	8/21/96	Europe	G03F	7/20	Yes
	B2	WO 01/23871 A1	4/05/01	PCT	G01N	21/95	Yes
	B3	WO 99/45340	9/10/99	PCT	G01B	11/02	Yes

**Other Art (Including Author, Title, Date Pertinent Pages, Etc.)**

Exam. Init.	Ref. Des.	Citation
	C1	Pfitzner <i>et al.</i> , "Cost reduction strategies for wafer expenditure," <i>Microelectronic Engineering</i> , 56:61-71, 2001
	C2	
	C3	

**EXAMINER:****DATE CONSIDERED:**

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.